

As a below named inventor, I declare :  
that I verily believe myself to be the original, first and sole (if only one individual inventor is listed below) or an original, first and joint inventor (if more than one individual inventor is listed below ) of the invention in

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND METHOD OF OUTPUTTING  
SIGNALS ON SEMICONDUCTOR INTEGRATED CIRCUIT

the specification of which is attached hereto unless the following box is checked.

☒ was filed on August 15, 2005 as United States Application  
or PCT International Application No. PCT/JP2005/015173 , and  
was amended on \_\_\_\_\_ (if applicable).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above. I acknowledge the duty to disclose information of which is material to patentability as defined in 37 CFR 1.56.

I hereby claim foreign priority benefits under 35 U.S.C. 119(a)-(d) or 365(b) of any foreign application(s) for patent or inventor's certificate, or 35 U.S.C. 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed:

<u>Country</u>	<u>Category</u>	<u>Application Number</u>	<u>Filing Date</u>	<u>Priority Claim</u>
Japan	Patent	2005-079562	March 18, 2005	Yes

I hereby appoint as my attorneys, with full power of substitution and revocation, to prosecute this application and transact all business in the Patent and Trademark Office connected therewith the practitioners at

Customer Number : 44654

of Sprinkle IP Law Group. Please address all correspondence and telephone communications to the address and telephone number for this Customer Number.

I declare further that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

DECLARATION FOR PATENT APPLICATION

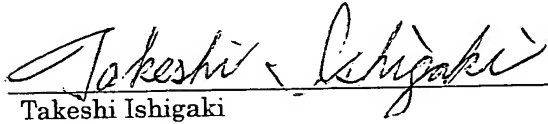
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(04S1230P)

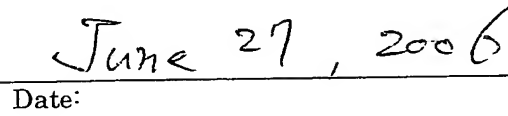
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